

Rajiv Gandhi Institute of Technology, Kottayam

No. D3/826/16/RIT

Dated: 25.04.2017

TENDER SCHEDULE

Superscription	: Tender No.D3/826/16/RIT, Purchase of TMS320F28335 based DSP development board with JTAG for the use of EEE Department
Last date and time receipt of tender	: 14/06/2017 11.30 a.m
Date and time of opening of tender	: 14/06/2016. 02.30 p.m
Last date and time of sale of tender form	: 13/06/2017 02.30 p.m
Date upto which the rates are to be firm	: 31/03/2018
Cost of tender form	: Original Rs.420/- inclusive of 5% VAT Duplicate Rs.210/- -do- By Post Rs.Rs.455/- includes VAT & postal charges
Address of the Officer from whom Tender Forms are to be obtained to whom tenders are to be send	: THE PRINCIPAL, RAJIV GANDHI INSTITUTE OF TECHNOLOGY, VELLOOR P.O. PAMPADY, KOTTAYAM KERALA, PIN - 686 501

List of Items Required

<u>Details of items</u>	<u>Quantity</u>
1 List attached	

PRINCIPAL

Sl.No.	Item	Specification	Quantity
1	TMS320F28335 based DSP development board with JTAG	<p>TMS320F28335 based DSP development board with 4 -channel 8 bit-parallel DAC out put, 4-channel 12 bit- 12CDAC output, 15 ADC inputs, 12 channel PWM outputs, Push button switches, LED in, on - board incremental encoder based rotary switch, USB connectivity and XDS100 JTAG.</p> <p>User manual with complete schematics and sample programmes must be supplied. One year on-site warranty.</p>	5